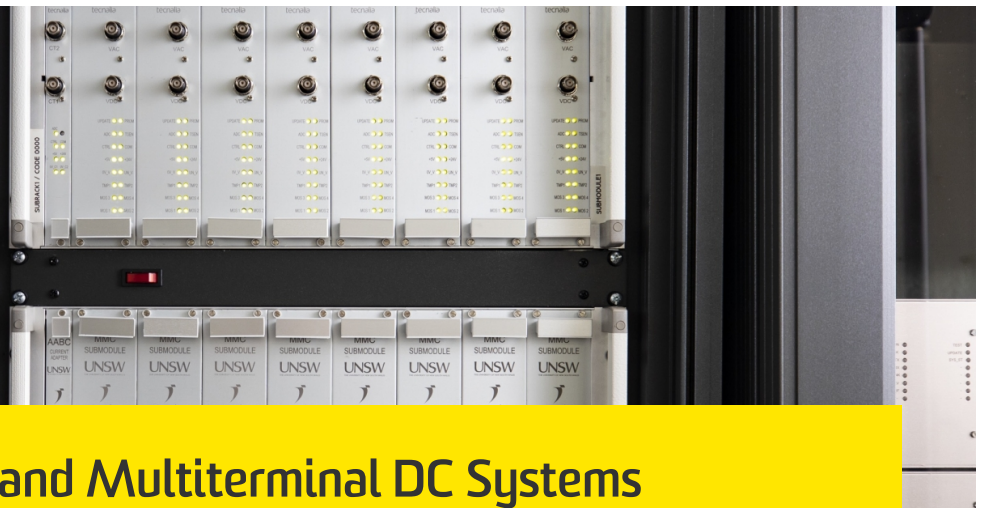




UNSW
SYDNEY



HVDC Transmission and Multiterminal DC Systems

Real-time simulation and hardware-in-the-loop testing expedites innovative solutions for interconnecting electricity grids over long distances, the integration of large-scale remote renewables, addressing intermittency and the formation of super grids.

Competitive advantage

- Expertise in:
 - Offline and real-time Phasor and EMT simulations
 - Power and control hardware-in-the-loop testing
- Wide range of experience in advanced HVDC converter models in multiple computational domains, ranging from fully average to component average, to detailed switching models
- State-of-the art and emerging converter technologies
- Extended simulation capability

Impact

- Contribution to AEMO's Integrated System Plan – 2018
- Submission to AEMC's Generation and Transmission Investment consultation – 2018

Capabilities and facilities

- The largest real-time digital simulator in Australia
- Fully configurable 4-terminal multiterminal HVDC hardware prototype
- Integration of simulators with laboratory hardware

More Information

Dr Harith Wickramasinghe

School of Electrical Engineering and Telecommunications

E: harith@unsw.edu.au

Dr Georgios Konstantinou

School of Electrical Engineering and Telecommunications

T: +61 (0) 2 9385 7405

E: g.konstantinou@unsw.edu.au

UNSW Knowledge Exchange

knowledge.exchange@unsw.edu.au

www.capabilities.unsw.edu.au

+61(2) 9385 5008